

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#31 Dps
non
2/2/02

Applicant(s): YOSHIHARA et al.	Atty. Dkt.: 15-015
Serial No.: Unknown	Group Art Unit:
Filed: Concurrently herewith	Examiner:
Title: SEMICONDUCTOR SENSOR CHIP HAVING DIAPHRAGM AND METHOD OF MANUFACTURING THE SAME	

Assistant Commissioner for Patents
Washington, D.C. 20231

Date: October 16, 2001

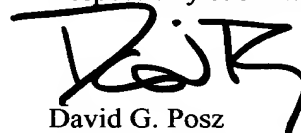
INFORMATION DISCLOSURE STATEMENT

Sir:

Pursuant to 37 C.F.R. §1.56, the reference(s) listed on the attached Form PTO-1449 is/are being submitted for consideration by the Examiner without any admission that it/they constitute(s) statutory prior art, or without any admission that it/they contain(s) subject matter that anticipates the invention or renders the invention obvious to a person of ordinary skill in the art.

The Examiner is requested to initial the attached PTO Form-1449 and to return a copy of same to the undersigned attorney as proof that the listed reference(s) has/have been considered and made of record.

Respectfully submitted,



David G. Posz
Reg. No. 37,701

Law Office of David G. Posz
2000 L Street, N.W.
Suite 200
Washington, D.C. 20036
(202) 416-1638 (phone)
Customer No. 23400